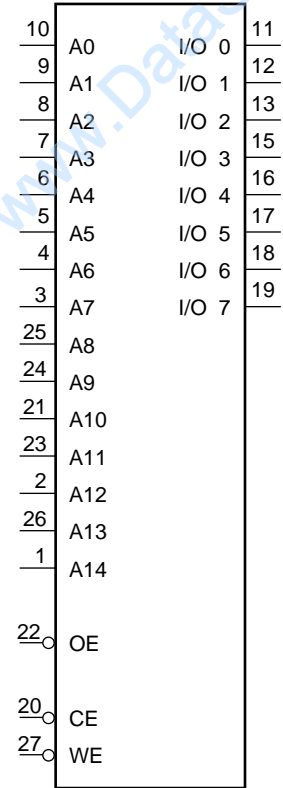
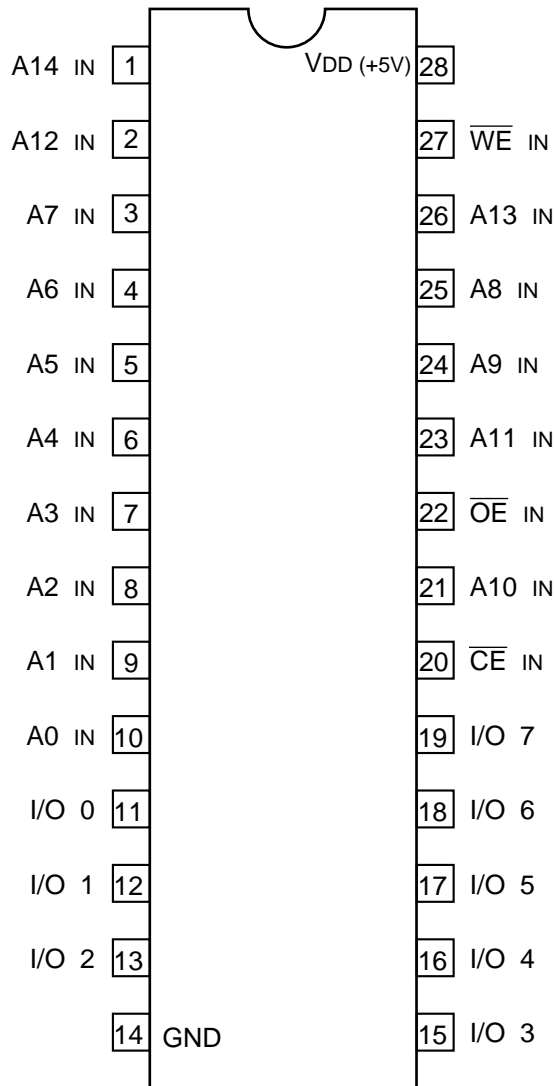

HN58C256P-20(1/2)
IL00

C-MOS 256K (32768x8)-BIT EEPROM
—TOP VIEW—



MODE \ PIN	\overline{CE}	\overline{OE}	\overline{WE}	I/O
READ	0	0	1	DOUT
STANDBY	1	X	X	HI-Z
WRITE	0	1	0	DIN
DESELECT	0	1	1	HI-Z
WRITE INHIBIT	X	X	1	—
WRITE INHIBIT	X	0	X	—

A0-A14 ; ADDRESS INPUT
I/O0-I/O7 ; DATA INPUT/OUTPUT
 \overline{CE} ; CHIP ENABLE INPUT
 \overline{OE} ; OUTPUT ENABLE INPUT
 \overline{WE} ; WRITE ENABLE INPUT

0 ; LOW LEVEL
1 ; HIGH LEVEL
X ; DON'T CARE
HI-Z ; HIGH IMPEDANCE

